



SPP1021

Dual P-Channel Enhancement Mode MOSFET

DESCRIPTION

The SPP1021 is the Dual P-Channel enhancement mode power field effect transistors are produced using high cell density, DMOS trench technology. This high density process is especially tailored to minimize on-state resistance and provide superior switching performance. These devices are particularly suited for low voltage applications such as notebook computer power management and other battery powered circuits where high-side switching, low in-line power loss, and resistance to transients are needed.

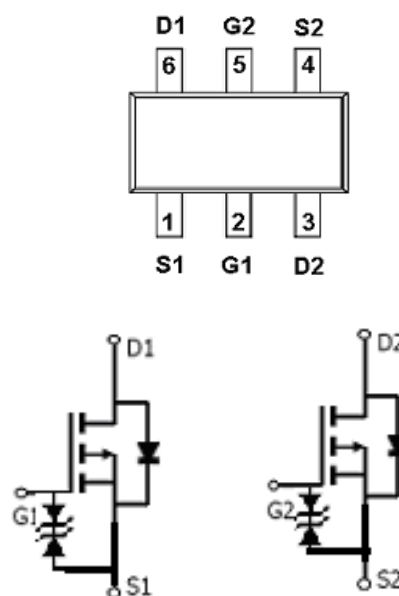
APPLICATIONS

- Power Management in Note book
- Portable Equipment
- Battery Powered System
- DC/DC Converter
- Load Switch
- DSC
- LCD Display inverter

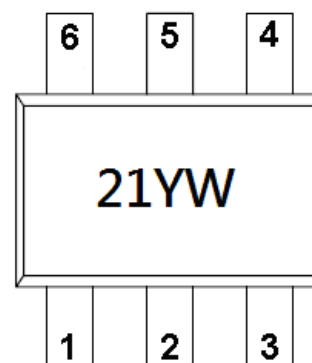
FEATURES

- ◆ P-Channel
 - 20V/0.45A, $R_{DS(ON)}=520m\Omega@V_{GS}=-4.5V$
 - 20V/0.35A, $R_{DS(ON)}=700m\Omega@V_{GS}=-2.5V$
 - 20V/0.25A, $R_{DS(ON)}=1500m\Omega@V_{GS}=-1.8V$
- ◆ Super high density cell design for extremely low $R_{DS(ON)}$
- ◆ Exceptional on-resistance and maximum DC current capability
- ◆ ESD protected
- ◆ SOT-563 (SC-89-6L) package design

PIN CONFIGURATION (SOT-563 / SC-89-6L)



PART MARKING





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PIN DESCRIPTION

Pin	Symbol	Description
1	S1	Source 1
2	G1	Gate 1
3	D2	Drain 2
4	S2	Source 2
5	G2	Gate 2
6	D1	Drain1

ORDERING INFORMATION

Part Number	Package	Part Marking
SPP1021S56RGB	SOT-563	21

※ Week Code : A ~ Z(1 ~ 26) ; a ~ z(27 ~ 52)

※ SPP1021S56RGB : Tape Reel ; Pb – Free ; Halogen – Free

ABSOLUTE MAXIMUM RATINGS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Typical	Unit
Drain-Source Voltage	VDSS	-20	V
Gate –Source Voltage	VGSS	±12	V
Continuous Drain Current(TJ=150°C)	ID	-0.45	A
		-0.35	
Pulsed Drain Current	IDM	-1.0	A
Continuous Source Current(Diode Conduction)	IS	-0.3	A
Power Dissipation	PD	0.35	W
		0.19	
Operating Junction Temperature	TJ	-55/150	°C
Storage Temperature Range	TSTG	-55/150	°C



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ELECTRICAL CHARACTERISTICS

(T_A=25°C Unless otherwise noted)

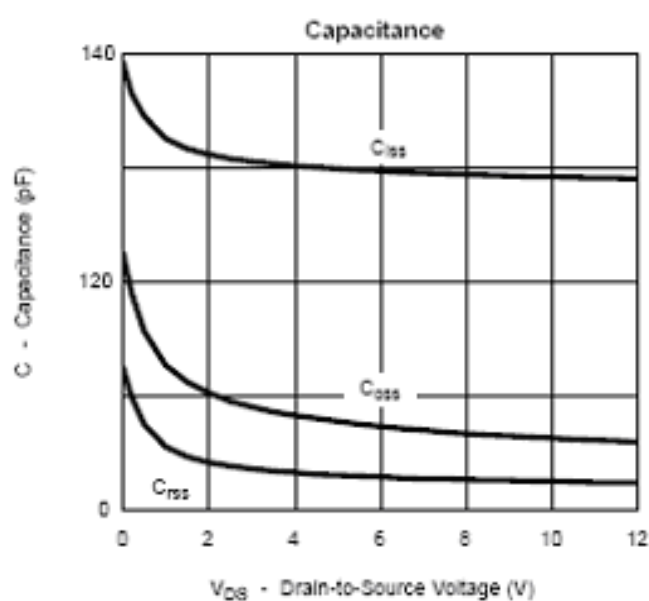
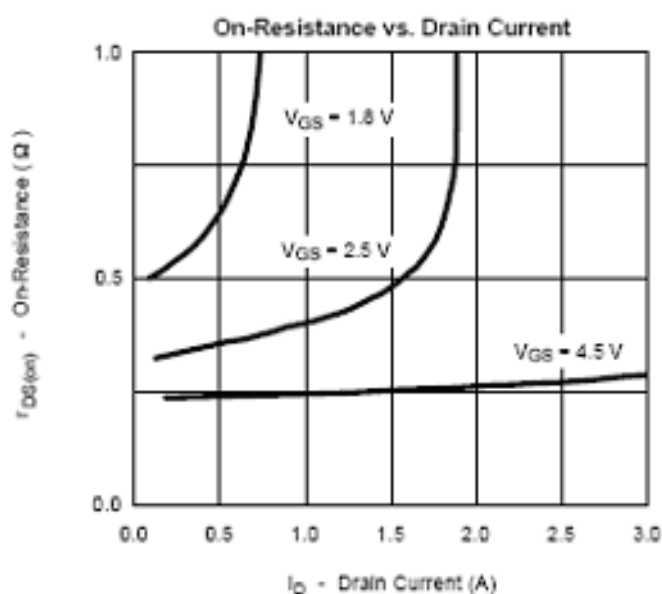
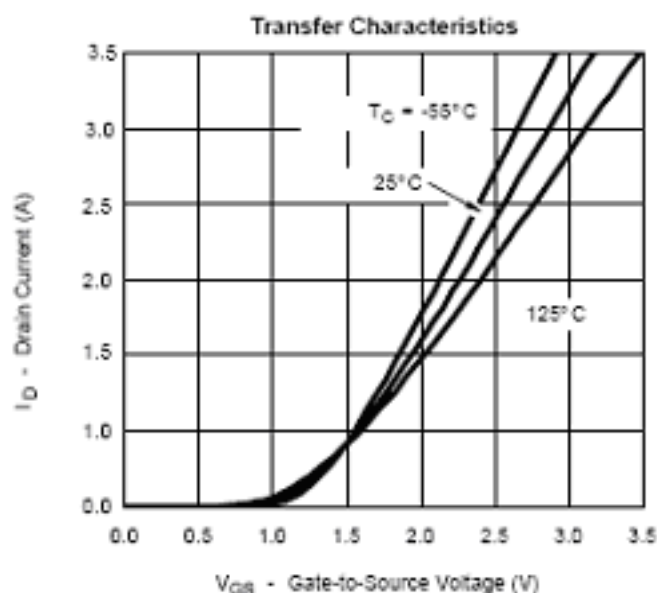
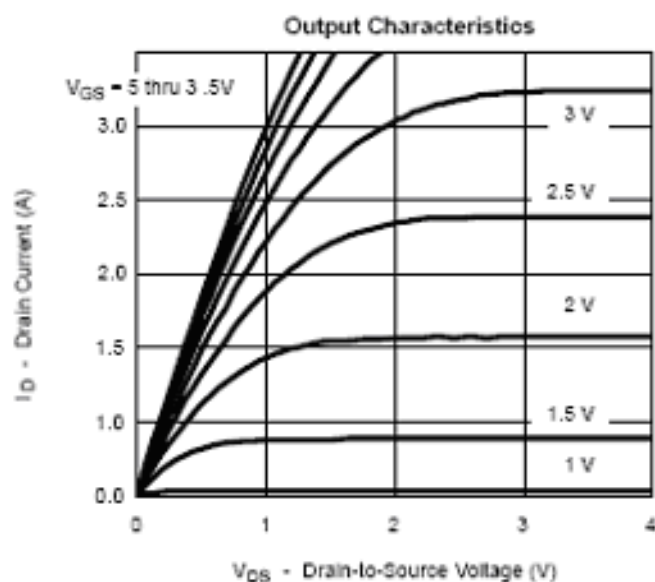
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =-250uA	-20			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250uA	-0.35		-1.0	
Gate Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±12V			±10	uA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-20V, V _{GS} =0V			-1	uA
		V _{DS} =-20V, V _{GS} =0V T _J =55°C			-5	
On-State Drain Current	I _{D(on)}	V _{DS} ≤-4.5V, V _{GS} =-5V	-0.7			A
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} =-4.5V, I _D =-0.45A		0.42	0.52	Ω
		V _{GS} =-2.5V, I _D =-0.35A		0.58	0.70	
		V _{GS} =-1.8V, I _D =-0.25A		0.95	1.5	
Forward Transconductance	g _{fs}	V _{DS} =-10V, I _D =-0.25A		0.4		S
Diode Forward Voltage	V _{SD}	I _S =-0.15A, V _{GS} =0V		-0.8	-1.2	V
Dynamic						
Total Gate Charge	Q _g	V _{DS} =-10V, V _{GS} =-4.5V I _D =-0.6A		1.5	2.0	nC
Gate-Source Charge	Q _{gs}			0.3		
Gate-Drain Charge	Q _{gd}			0.35		
Turn-On Time	t _{d(on)}	V _{DD} =-10V, R _L =10Ω, I _D =-0.4A V _{GEN} =-4.5V, R _G =6Ω		5	10	nS
	t _r			15	25	
Turn-Off Time	t _{d(off)}			8	15	
	t _f			1.4	1.8	



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TYPICAL CHARACTERISTICS

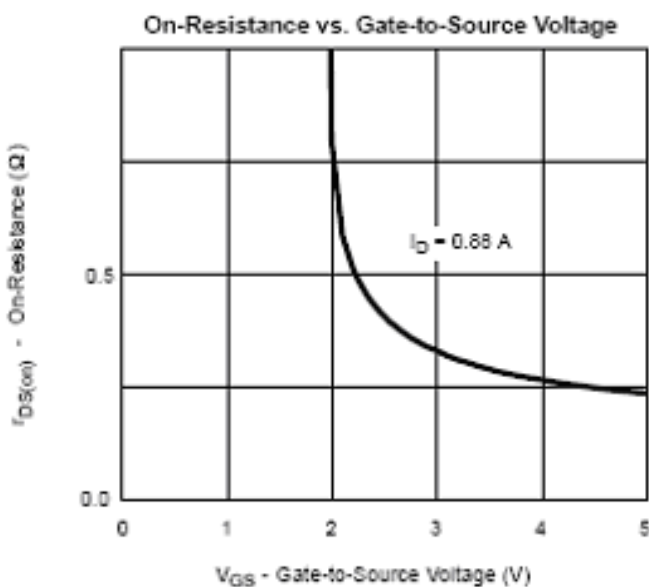
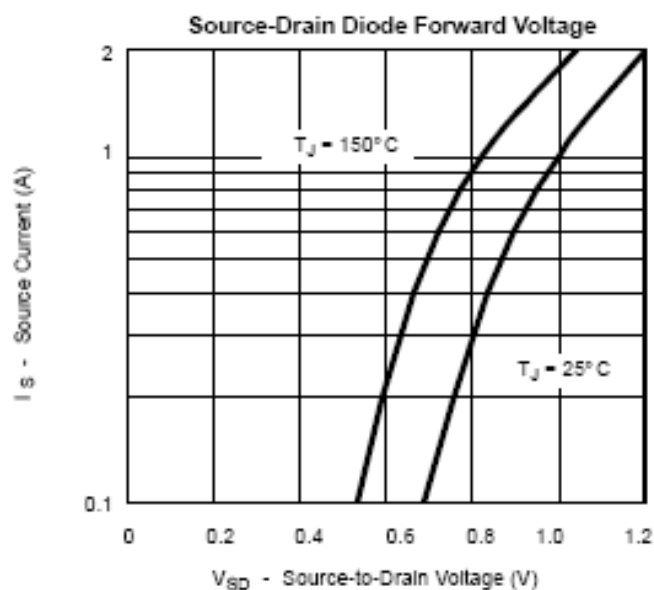
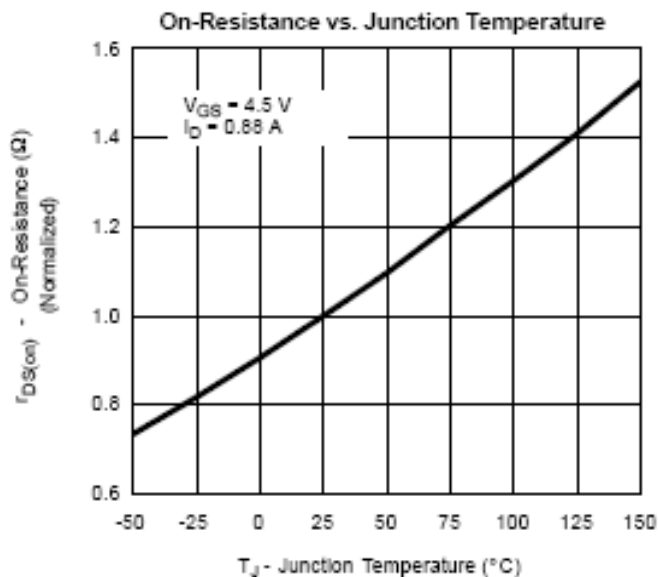
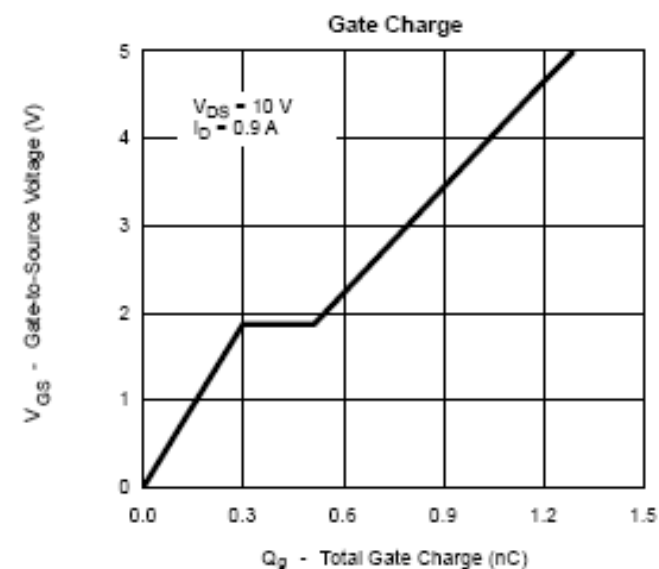




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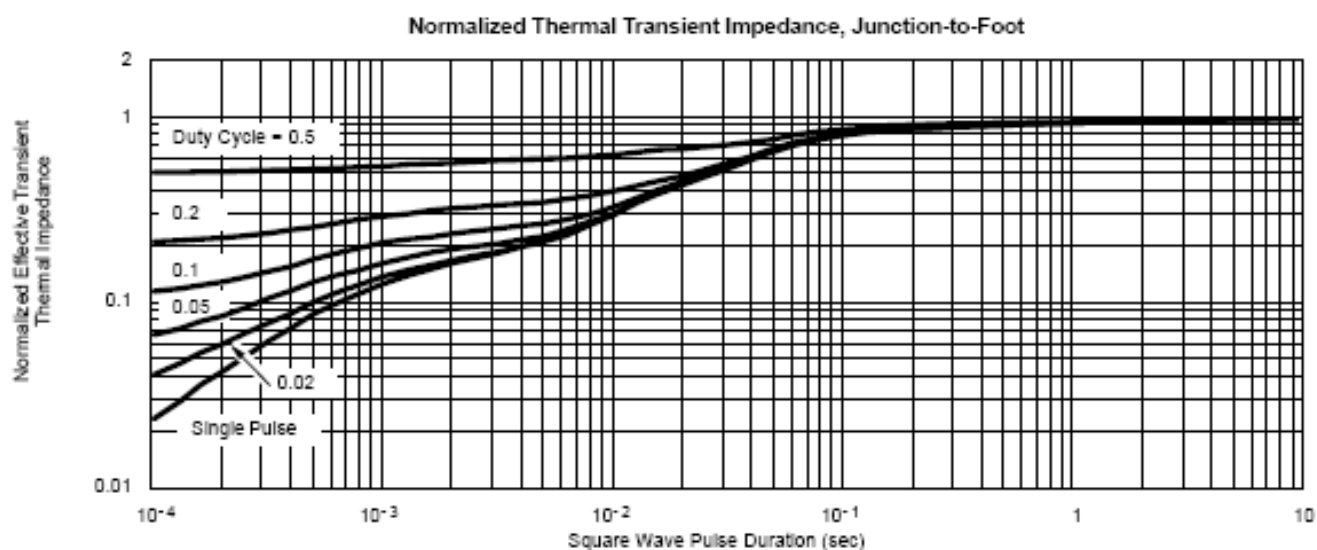
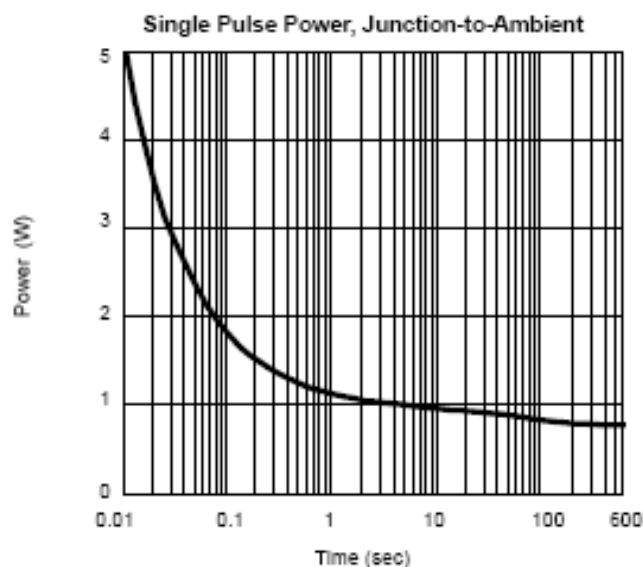
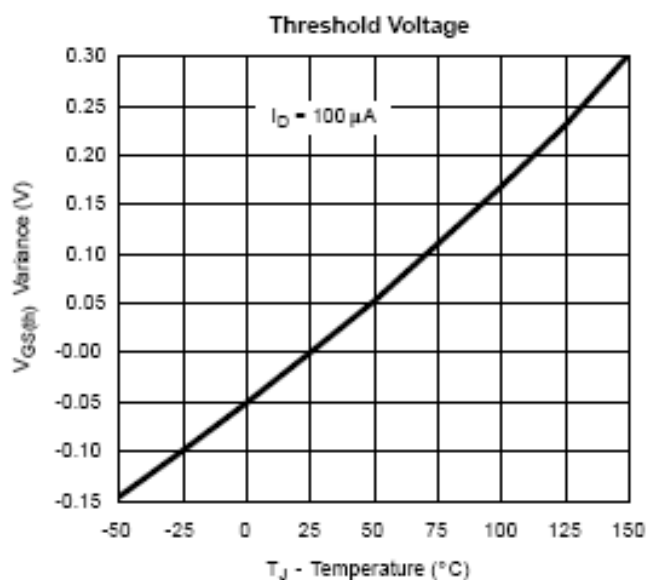




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TYPICAL CHARACTERISTICS





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SYNC Power Corporation

7F-2, No.3-1, Park Street

NanKang District (NKSP), Taipei, Taiwan 115

Phone: 886-2-2655-8178

Fax: 886-2-2655-8468

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